

Description:

Key-Value Store (KVS) is an essential service for multiple applications. Telecom directories, Internet Protocol forwarding tables, and de-duplicating storage systems, for example, all need key-value tables to associate data with unique identifiers. In datacenters, high performance KVS tables allow hundreds or thousands of machines to easily share data by simply associating values with keys and allowing client machines to read and write those keys and values over standard high-speed Ethernet.

Examples:	Key	Value
Directory	Company Algo-Logic	Phone # (408) 707-3740
Forwarding Tables	IP Address 204.2.34.5	Interface : MAC Address Eth6 : 02:33:29:F2:AB:CC
Data De-duplication	Content Hash XYZ	Storage Block ID 948830038411
Stock Trading	Order ID ATY1121791101	Symbol, Side, Price AAPL, B, 126.75
Graph Search	Virtex v140	Edge List v201, v206, v225

Algo-Logic's KVS leverages Gateway Defined Networking® (GDN) on Field Programmable Gate Arrays (FPGAs) to perform lookups with the lowest latency (less than 1 microsecond), with the highest throughput, and the least processing energy. Deploying GDN solutions save network operators' time, cost, and power resulting in significantly lower Total Cost of Ownership (TCO).

Applications and Use-cases:

- Telecom ESN and SIM key value tables
- IPv4 or IPv6 Internet addresses
- Block store caching
- Keyword search
- NoSQL database acceleration
- N-Tuple lookups
- World Wide Web cookie keys
- User identifiers (UID, SSN, logins)
- Stock market order IDs
- Pattern matching

Key Features:

- In memory NoSQL database with search rates of up to 150 Million Searches Per Second
- Under 0.4µs latency using fast tables
- Compatible with C++/OpenCL host API
- Open standard message format integrates with multi-language client software applications
- Ideal for receiving real-time data from acquisition systems such as the Algo-Logic Black Diamond Rackmount (BDR-3) and

Hardware Platform

- Pre-programmed gateway application on a half-height or full-height expansion card that fits into any standard server
- Portable gateway supported on most commercially available FPGA card platforms, including the Intel® PAC data center acceleration card



Software Controller API Options

- Client software API compatible with C/C++, Java, Python, and other programming languages

GDN-Search Reference Design Metrics:

KVS Search Rate	Up to 150 Million Searches Per Second (MSPS)
Table Depth	48K for fast tables using on-chip memory
Key Size	96 bits (12 Bytes) default
Value Size	96 bits (12 Bytes)
Latency (On-Chip)	Under 400 ns (excluding host bus latency)
Application Architecture	Custom RTL kernel within OpenCL platform
FPGA Devices Supported	Intel ® PAC
Target Application Markets	Real-time data, datacenters, connected cities, inference, ISP, security industries

Key Value Store (KVS) Architecture:

