

Low Latency Key-Value Store (KVS)

Gateway Defined Networking® for FPGA

Description

Key-Value Store (KVS) is an essential service for multiple applications. Telecom directories, Internet Protocol forwarding tables, and de-duplicating storage systems, for example, all need key-value tables to associate data with unique identifiers. In datacenters, high performance KVS tables allow hundreds or thousands of machines to easily share data by simply associating values with keys and allowing client machines to read and write those keys and values over standard high-speed Ethernet.

Examples:	Key	Value
Directory	Company Algo-Logic	Phone # (408) 707-3740
Forwarding Tables	IP Address 204.2.34.5	Interface : MAC Address Eth6 : 02:33:29:F2:AB:CC
Data De-duplication	Content Hash XYZ	Storage Block ID 948830038411
Stock Trading	Order ID ATY11217911101	Symbol, Side, Price AAPL, B, 126.75
Graph Search	Virtex v140	Edge List v201, v206, v225

Algo-Logic's KVS leverages Gateway Defined Networking® (GDN) on Field Programmable Gate Arrays (FPGAs) to perform lookups with the lowest latency (sub-microsecond), highest throughput, and least processing energy. Deploying GDN solutions save network operators time, cost, and power resulting in significantly lower Total Cost of Ownership (TCO).

Applications

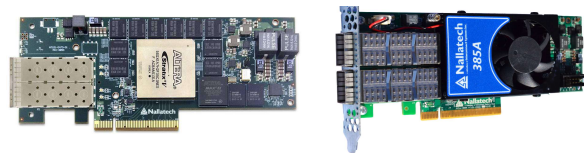
- Telecom ESN and SIM key value tables
- IPv4 or IPv6 Internet addresses
- Auto-completion
- NoSQL database acceleration
- Tuple lookups
- User preferences and profiles
- Stock market order IDs
- Inventory management
- Multiplayer game servers
- Real-time bidding for AdTech

Key Features

- FPGA accelerated ultra low latency search
- Sub μ -Joule/lookup energy consumption
- Deterministic and jitter-free processing
- 10 and 40 Gbps Ethernet line rate support
- Easy to integrate with client software via free, open-source multi-language APIs
- Supports standard create, read, update, and delete operations

Hardware Platforms

- Pre-programmed gateway application on a half-height or full-height expansion card that fits into any standard server
- Portable gateway supported on most commercially available FPGA card platforms



Software Controller API Options

- Free, open-source client software API compatible with C/C++, Java, Python, and other programming languages

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GDN-Search Reference Design Metrics

Key/Value Search Rate	Up to 170 MSPS (Million Searches Per Second) per FPGA card
Table Depth	From 48K entries in on-chip RAM to 12M entries per EMSE2 core with DDR3*
Key Size	96 bits
Value Size	96 bits for fast table, 352 bits for large table
Latency	Sub 500 nanoseconds (~88x less latency than with sockets)
Throughput	Line-rate network interface speeds of 10GE to 40GE
Power Consumption Rate	Less than 0.52 μ -Joules/message (~21x less than with software sockets)
FPGA Devices Supported	Altera Stratix V A5, A7, Arria 10
Platforms Supported	Terasic DE5-Net, Nallatech P385 & P385A, Bittware S5PH-Q

* Actual number of entries accessible depends on values of the keys

GDN-Search Block Diagram

