

Ultra Low Latency Key-Value Store (KVS)

Gateway Defined Networking® in Intel® Xeon® + FPGA

Description

Key-Value Store (KVS) is an essential service for multiple applications. Telecom directories, Internet Protocol forwarding tables, and de-duplicating storage systems, for example, all need key-value tables to associate data with unique identifiers. In datacenters, high performance KVS tables allow hundreds or thousands of machines to easily share data by simply associating values with keys and allowing client machines to read and write those keys and values over standard high-speed Ethernet.

Examples:	Key	Value
Directory	Company Algo-Logic	Phone # (408) 707-3740
Forwarding Tables	IP Address 204.2.34.5	Interface : MAC Address Eth6 : 02:33:29:F2:AB:CC
Data De-duplication	Content Hash XYZ	Storage Block ID 948830038411
Stock Trading	Order ID ATY11217911101	Symbol, Side, Price AAPL, B, 126.75
Graph Search	Virtex v140	Edge List v201, v206, v225

Algo-Logic's KVS leverages Gateway Defined Networking® (GDN) on Field Programmable Gate Arrays (FPGAs) to perform lookups with the lowest latency (sub-microsecond), highest throughput, and least processing energy. Deploying GDN solutions save network operators time, cost, and power resulting in significantly lower Total Cost of Ownership (TCO).

Applications

- Telecom ESN and SIM key value tables
- IPv4 or IPv6 Internet addresses
- Auto-completion
- NoSQL database acceleration
- Tuple lookups
- User preferences and profiles
- Stock market order IDs
- Inventory management
- Multiplayer game servers
- Real-time bidding for AdTech

Key Features

- FPGA accelerated ultra low latency search
- Sub μ -Joule/lookup energy consumption
- Deterministic and jitter-free processing
- 10 and 40 Gbps Ethernet line rate support
- Easy to integrate with client software via free, open-source multi-language APIs
- Supports standard create, read, update, and delete operations

Hardware Platforms

- Pre-programmed gateway application for Intel® Xeon® + FPGA Server Platform with E5-2600 v4 processor (with 14 cores at 2.4GHz) with integrated Altera® Arria® 10 GX1150 FPGA
- Portable gateway supported on most commercially available FPGA PCIe card platforms

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Intel® Xeon® + FPGA Server System

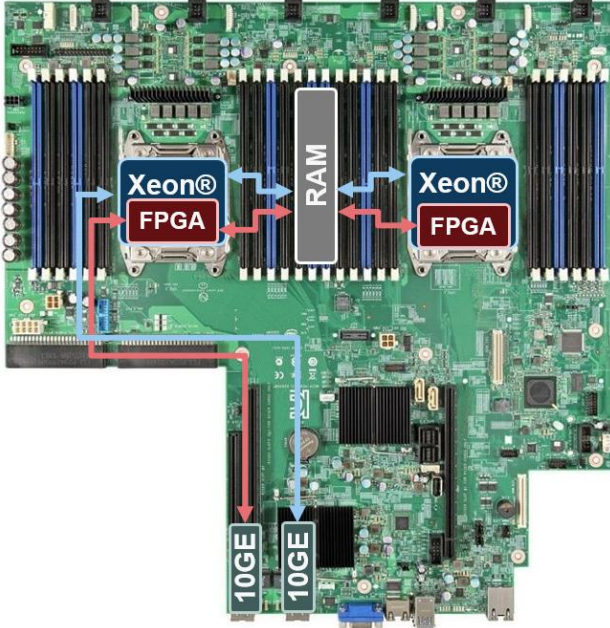


Figure 1. Intel® Xeon® + FPGA Motherboard

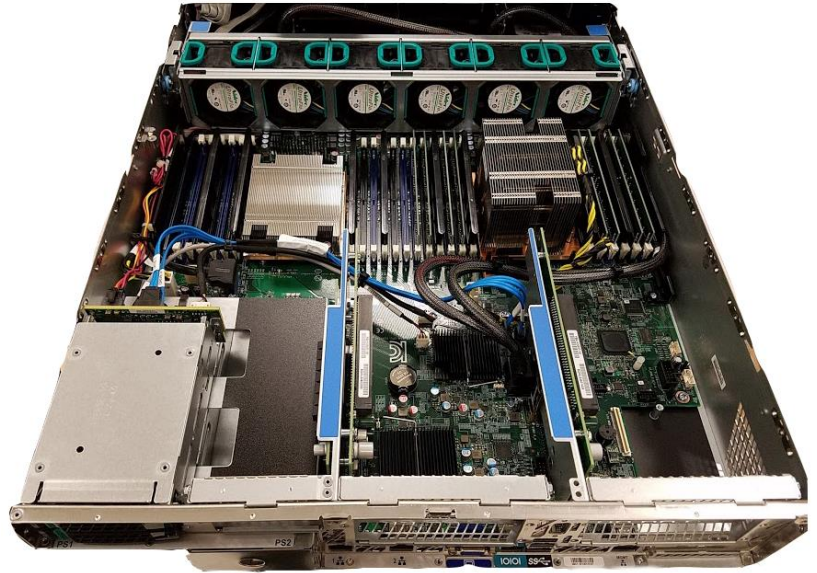


Figure 2. Intel® Xeon® + FPGA Server System

Key-Value Store in Intel® Xeon® + FPGA Block Diagram

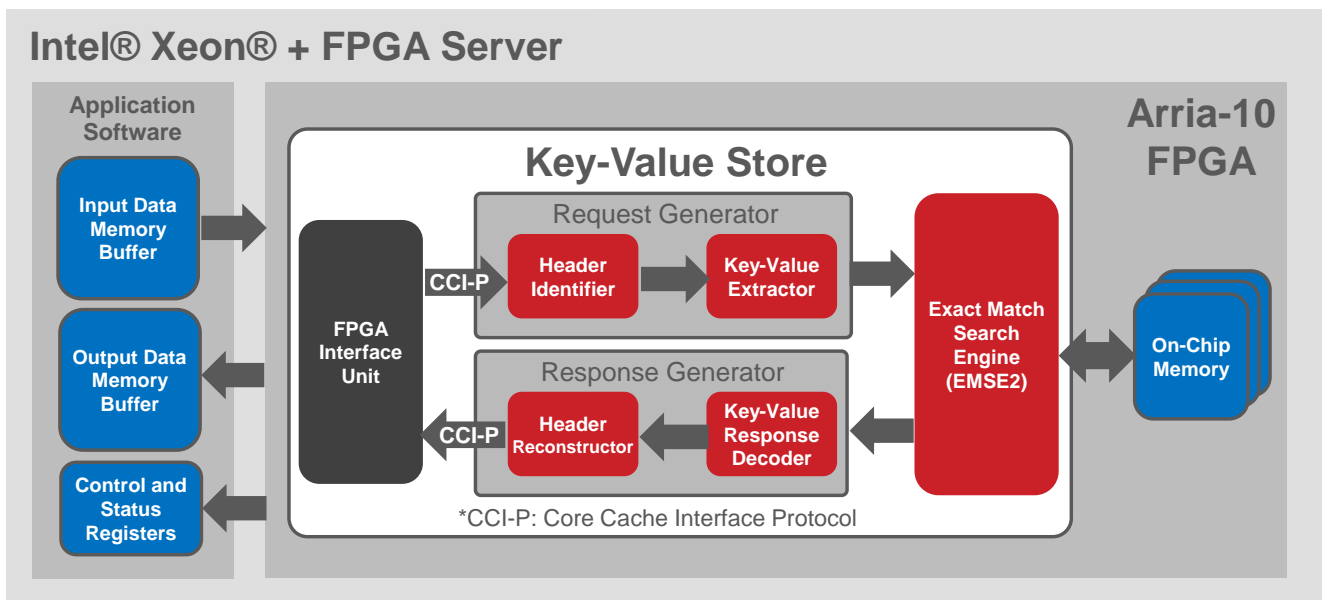


Figure 3. Key-Value Store Block Diagram